

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments	Error Definition
3	BRS	L7	1	delay adj gates same skew same constraint	USPAT; US-PGPUB; DERWENT	2002/07/15 10:09		
4	BRS	L11	104	skew same clock adj tree same delay	USPAT; US-PGPUB; DERWENT	2002/07/15 10:18		
5	BRS	L15	39	11 and adjust	USPAT; US-PGPUB; DERWENT	2002/07/15 10:11		
6	BRS	L19	75	clock same tree same synthesis	USPAT; US-PGPUB; DERWENT	2002/07/15 10:18		
7	BRS	L23	17	(clock adj tree adj synthesis) and skew and buffers and delay	USPAT; US-PGPUB; DERWENT	2002/07/15 10:19		